

A Hybrid Controller for Boost Inverter: Modeling, Simulation, and Experimental Results

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Abstract: Some controls have been introduced to control boost inverter, e.g. linear control, sliding mode control, and energy shaping based control. In this paper, the authors propose boost inverter control based on hybrid model. It is easier to design because it does not require complex mathematical formula. Simulations and experiments have been conducted to determine the performance of the proposed control under steady state and during transient state with a variety of load, inductance, and capacitance. The simulation and experimental results show that the hybrid control can work properly.

Keywords: Boost inverter, hybrid model, and control.

1. Introduction

Inverters are widely used in various applications such as uninterruptible power supply (UPS), motor drive, and utility interface. Buck inverter is commonly used to obtain an AC output from a DC source. One characteristic of this inverter is that the output voltage is lower than the DC input voltage. If the required voltage is greater than the input, a dc-dc boost converter must be added in the front of inverter [1]. The addition of dc-dc boost converter on buck inverter can increase the volume, weight, price, and losses.

To avoid that, it is necessary to use boost inverter. Boost inverter can be made of two dc-dc boost converters with load placed on each output of the dc-dc boost converter [2]. Compared to buck inverter equipped with a dc-dc boost converter, boost inverter circuit requires less switching components. Boost inverter can generate voltage that is greater or smaller than the input voltage. As a result, this inverter is more suitable to generate variable AC voltage with a wide range of amplitude variation.

The control that is used in [2] is a sliding mode control. There are two controls for each dc-dc boost converter. The sliding surface is formed by the inductor current error and output voltage error. Inductor current error is obtained from the inductor current passed to a high pass filter. The filter cut-off frequency is below the switching frequency in order to pass the switching ripple effect. To avoid chattering phenomena, the authors used a hysteresis in the sliding surface. The principle of current mode control for boost inverter was proposed in [3]. The authors compared the proposed control with sliding mode control. Experiments have been conducted in the condition of input voltage variation and short circuit. The experimental results showed that the sliding mode control cannot work properly when the input voltage changed and produces large transient currents during a short circuit. On the other hand, the control proposed in [3] could work well under those two conditions. A control method based on small-signal analysis has been introduced in [4]. The advantage of the control is that it only need a voltage sensor. The system is modeled by using a small signal model around the duty cycle of 0.5. Simulation results showed that the proposed control could work well when the input voltage changed. Energy shaping control also has been published [5]. However, this control uses a very complicated mathematical formula. The other sliding mode control was proposed in [6] to control both dc-dc converters simultaneously. Unlike some boost inverter controls that have been mentioned before, the proposed control only use a controller. The sliding surface consists of PI (Proportional and

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Integral) control of boost inverter output voltage error and inductor current error from both dc-dc boost converters. Simulation results showed that the voltage of each dc-dc converter is not sinusoidal, but the boost inverter output voltage is sinusoidal indeed. The other controls e.g. one cycle control and resonant controllers also have been proposed for boost inverter [7][8].

In this paper, the authors propose a hybrid model-based control to control the boost inverter. Compared to the boost inverter controls that have been proposed by previous researchers, this control is easier to design because it does not require complex mathematical formulas. Simulations and experiments were conducted to verify the performance of the proposed control.

2. Boost Inverter Hybrid Modeling

A hybrid model is a mathematical model that represents complex systems with a hierarchical structure and may consist of continuous and discrete subsystems that interact with each other. The evolution of discrete subsystem depends on the continuous information and the evolution of the continuous subsystem depends on the discrete information. A hybrid system is expressed by $H = (Q, X, f, I, E, G)$, where $Q = \{q_1, \dots, q_n\}$ is a set of discrete states, $X \subseteq R^n$ is continuous state spaces, $f: Q \rightarrow (X \rightarrow R^n)$ assign every discrete state to a Lipschitz continuous vector field on X , i.e. the dynamic equation $\dot{x} = f(x, t)$, $I: Q \rightarrow 2^X$ assign each $q \in Q$ an invariant set, $E \subseteq Q \times Q$ is a collection of discrete transitions, and $G: E \rightarrow 2^X$ is guard equation for each $e = (q, q') \in E$.

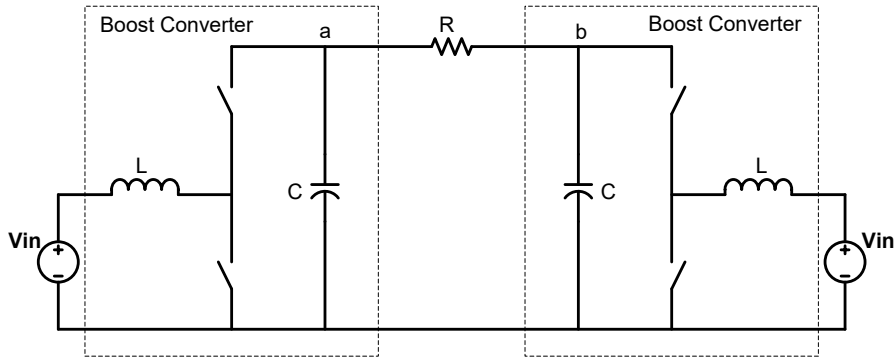


Figure 1. Boost inverter.

Power electronics circuit is one of hybrid system. The continuous state is the current and voltage of passive components such as capacitor and inductor. The discrete state is generated by the switching components such as MOSFET, IGBT, and diode. The control for power electronics hybrid model is guards that determine the transition of a discrete state to another discrete state, to obtain the desired continuous subsystem response [9].

The boost inverter topology in this study is shown in Figure. 1. The inverter consists of two dc-dc boost converters. The boost inverter model is shown in Figure. 2. The set of discrete state is $Q = \{q_1, q_2, q_3, q_4\}$ as shown in Figure. 3. There are four continuous states, i.e. $x = \{i_{La}, v_{oa}, i_{Lb}, v_{ob}\}$, where i_{La} is the inductor current of the first boost converter, v_{oa} is the output voltage of the first boost converter, i_{Lb} is the inductor current of the second boost converter, and v_{ob} is the output voltage of the second boost converter,

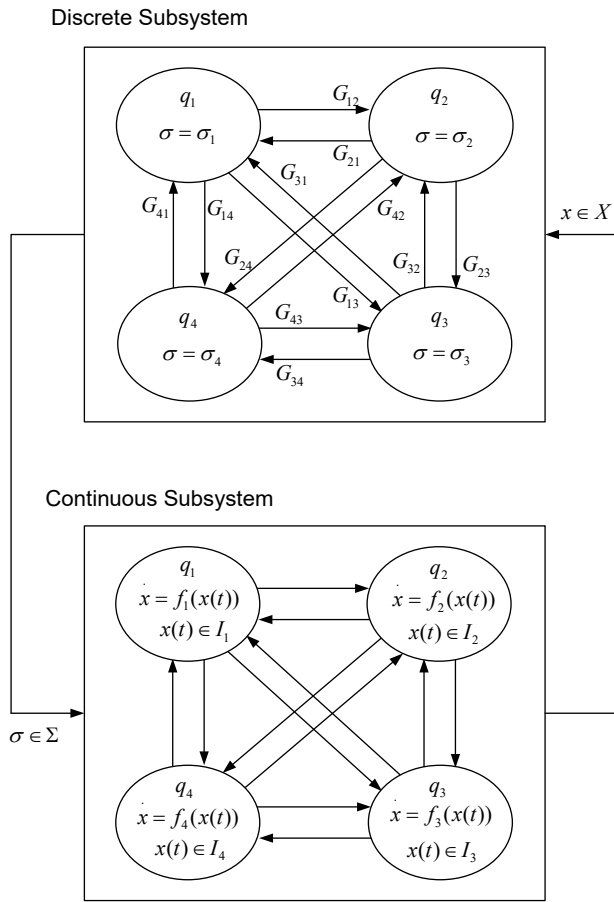


Figure 2. Boost inverter hybrid model

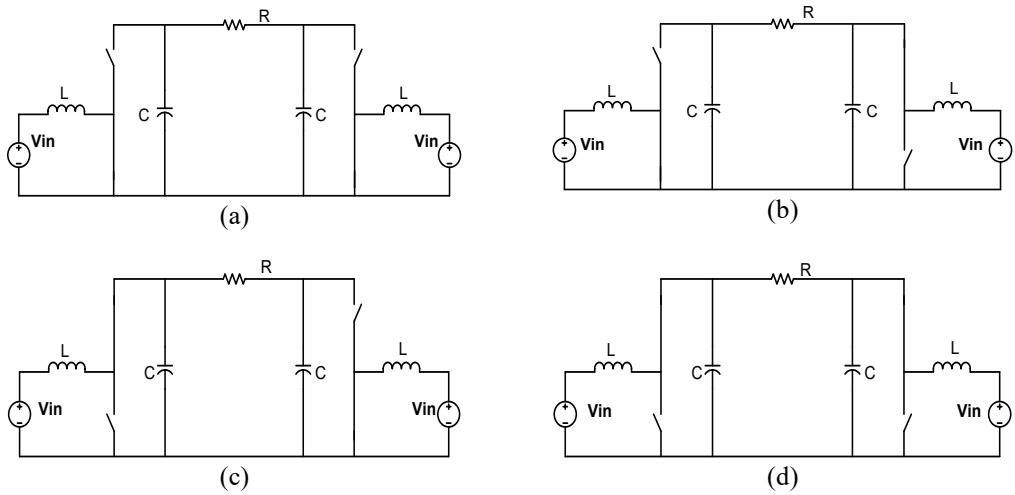


Figure 3. The discrete state of boost inverter (a) $Q = q_1$, (b) $Q = q_2$, (c) $Q = q_3$, and (d) $Q = q_4$

The Lipshitz vector field f_i for $Q = q_1$, $Q = q_2$, $Q = q_3$, and $Q = q_4$, each is shown in (1), (2), (3), and (4) respectively.

$$\dot{x} = f_1 = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & -\frac{1}{RC} & 0 & \frac{1}{RC} \\ 0 & 0 & 0 & 0 \\ 0 & \frac{1}{RC} & 0 & -\frac{1}{RC} \end{bmatrix} x + \begin{bmatrix} \frac{V_{in}}{L} \\ 0 \\ \frac{V_{in}}{L} \\ 0 \end{bmatrix} \quad (1)$$

$$\dot{x} = f_2 = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & -\frac{1}{RC} & 0 & \frac{1}{RC} \\ 0 & 0 & 0 & -\frac{1}{L} \\ 0 & \frac{1}{RC} & \frac{1}{C} & -\frac{1}{RC} \end{bmatrix} x + \begin{bmatrix} \frac{V_{in}}{L} \\ 0 \\ \frac{V_{in}}{L} \\ 0 \end{bmatrix} \quad (2)$$

$$\dot{x} = f_3 = \begin{bmatrix} 0 & -\frac{1}{L} & 0 & 0 \\ \frac{1}{C} & -\frac{1}{RC} & 0 & \frac{1}{RC} \\ 0 & 0 & 0 & 0 \\ 0 & \frac{1}{RC} & 0 & -\frac{1}{RC} \end{bmatrix} x + \begin{bmatrix} \frac{V_{in}}{L} \\ 0 \\ \frac{V_{in}}{L} \\ 0 \end{bmatrix} \quad (3)$$

$$\dot{x} = f_4 = \begin{bmatrix} 0 & -\frac{1}{L} & 0 & 0 \\ \frac{1}{C} & -\frac{1}{RC} & 0 & \frac{1}{RC} \\ 0 & 0 & 0 & -\frac{1}{L} \\ 0 & \frac{1}{RC} & \frac{1}{C} & -\frac{1}{RC} \end{bmatrix} x + \begin{bmatrix} \frac{V_{in}}{L} \\ 0 \\ \frac{V_{in}}{L} \\ 0 \end{bmatrix} \quad (4)$$

In (1)-(4), L is the inductance, C is the capacitance, R is the load resistance, and V_{in} is the input voltage.

The set of discrete transition is

$$E = \{(q_1, q_2)(q_1, q_3)(q_1, q_4)(q_2, q_1)(q_2, q_3)(q_2, q_4) \\ (q_3, q_1)(q_3, q_2)(q_3, q_4)(q_4, q_1)(q_4, q_2)(q_4, q_3)\} \quad (5)$$

The guard equations for the boost inverter are as follow

$$G(q_2, q_1) = \{x \in R^4 \mid \sigma = \sigma_1\} \quad (6)$$

$$G(q_3, q_1) = \{x \in R^4 \mid \sigma = \sigma_1\} \quad (7)$$

$$G(q_4, q_1) = \{x \in R^4 \mid \sigma = \sigma_1\} \quad (8)$$

$$G(q_1, q_2) = \{x \in R^4 \mid \sigma = \sigma_2\} \quad (9)$$

$$G(q_3, q_2) = \{x \in R^4 \mid \sigma = \sigma_2\} \quad (10)$$

$$G(q_4, q_2) = \{x \in R^4 \mid \sigma = \sigma_2\} \quad (11)$$

$$G(q_1, q_3) = \{x \in R^4 \mid \sigma = \sigma_3\} \quad (12)$$

$$G(q_2, q_3) = \{x \in R^4 \mid \sigma = \sigma_3\} \quad (13)$$

$$G(q_4, q_3) = \{x \in R^4 \mid \sigma = \sigma_3\} \quad (14)$$

$$G(q_1, q_4) = \{x \in R^4 \mid \sigma = \sigma_4\} \quad (15)$$

$$G(q_2, q_4) = \{x \in R^4 \mid \sigma = \sigma_4\} \quad (16)$$

$$G(q_3, q_4) = \{x \in R^4 \mid \sigma = \sigma_4\} \quad (17)$$

The σ_i are defined in the next section.

3. Boost Inverter Control

To control the boost inverter, each dc-dc converter must produce a unipolar sinusoidally dc-biased voltage. To maximize the voltage deviation at the load, the following references are used.

$$v_{oaref} = V_{dc} + \frac{V_r}{2} \sin(\omega t) \quad (18)$$

$$v_{obref} = V_{dc} - \frac{V_r}{2} \sin(\omega t) \quad (19)$$

Where v_{oaref} is the output voltage reference of the first boost converter, v_{obref} is the output voltage reference of the second boost converter, and V_r is amplitude of the boost inverter reference. The value of V_{dc} can be chosen arbitrary, but it must fulfill the following requirements.

$$V_{dc} \geq v_{in} + \frac{V_r}{2} \quad (20)$$

The overall control is shown in Figure. 4. The hybrid control block is fed by the states $x = \{i_{La}, v_{oa}, i_{Lb}, v_{ob}\}$ and the references $x_d = \{i_{La ref}, v_{oaref}, i_{Lb ref}, v_{obref}\}$. The PI controls are used to produce the current references. There are two PI controls, each is fed by an error between the voltage reference v_{oaref} and v_{obref} and the output voltage v_{oa} and v_{ob} .

The hybrid control minimizes the cosine between $x - x_d$ and $f_i(x)$ by using the following algorithm [10]-[12].

$$\sigma_i = \arg \min_{i \in \Lambda} \frac{\langle x - x_d, f_i(x) \rangle}{\|x - x_d\| \|f_i(x)\|} \quad (21)$$

Where $e = x - x_d$. Since $\|x - x_d\|$ does not depend on i , it is ignored in the control computation. By selecting the minimum value of cosine between the vectors $f(x)$ and $e = x - x_d$, it will make x closer to the desired x_d . The pseudo-code of the control algorithm is shown in Figure. 5.

4. Simulation Results

PSIM simulations were conducted by using boost inverter parameters as shown in Table 1. The voltage references of the boost converters are

$$v_{oaref} = 320 + 156 \sin(100\pi t) \quad (22)$$

$$v_{obref} = 320 - 156 \sin(100\pi t) \quad (23)$$

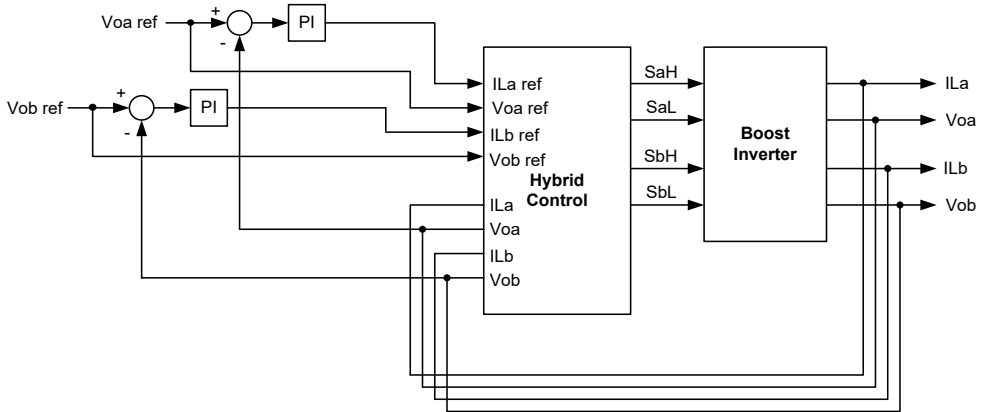


Figure 4. Block diagram of boost inverter control

```

// Sa = 0; Sb = 0;
fla1 = vin/L;
f2a1 = -(voa - vob)/(R*C);
flb1 = vin/L;
f2b1 = (voa - vob)/(R*C);
flmag = sqrt(fla1*fla1 + f2a1*f2a1 + flb1*flb1 + f2b1*f2b1);

// Sa = 0; Sb = 1;
fla2 = vin/L;
f2a2 = -(voa - vob)/(R*C);
flb2 = (vin - vob)/L;
f2b2 = (voa - vob)/(R*C) + iLb/C;
f2mag = sqrt(fla2*fla2 + f2a2*f2a2 + flb2*flb2 + f2b2*f2b2);

// Sa = 1; Sb = 0;
fla3 = (vin - voa)/L;
f2a3 = -(voa - vob)/(R*C) + iLa/C;
flb3 = vin/L;
f2b3 = (voa - vob)/(R*C);
f3mag = sqrt(fla3*fla3 + f2a3*f2a3 + flb3*flb3 + f2b3*f2b3);

// Sa = 1; Sb = 1;
fla4 = (vin - voa)/L;
f2a4 = -(voa - vob)/(R*C) + iLa/C;
flb4 = (vin - vob)/L;
f2b4 = (voa - vob)/(R*C) + iLb/C;
f4mag = sqrt(fla4*fla4 + f2a4*f2a4 + flb4*flb4 + f2b4*f2b4);

deltaia = iLa - iLaref;
deltaib = iLb - iLbref;
deltava = voa - voaref;
deltavb = vob - vobref;

delta1 = (deltaia*fla1 + deltava*f2a1 + deltaib*flb1 + deltavb*f2b1)/flmag;
delta2 = (deltaia*fla2 + deltava*f2a2 + deltaib*flb2 + deltavb*f2b2)/f2mag;
delta3 = (deltaia*fla3 + deltava*f2a3 + deltaib*flb3 + deltavb*f2b3)/f3mag;
delta4 = (deltaia*fla4 + deltava*f2a4 + deltaib*flb4 + deltavb*f2b4)/f4mag;

Sa = 0; Sb = 0;
if (delta1 < delta2 && delta1 < delta3 && delta1 < delta4)
{
    Sa = 0;
    Sb = 0;
}
if (delta2 < delta1 && delta2 < delta3 && delta2 < delta4)
{
    Sa = 0;
    Sb = 1;
}
if (delta3 < delta1 && delta3 < delta2 && delta3 < delta4)
{
    Sa = 1;
    Sb = 0;
}
if (delta4 < delta1 && delta4 < delta2 && delta4 < delta3)
{
    Sa = 1;
    Sb = 1;
}

```

Figure 5. Pseudo-code of hybrid control

Table 1. Boost inverter parameters

Parameters	Values
Input voltage	150 V
Output voltage amplitude	312 V
Output fundamental frequency	50 Hz
Inductor	150 μ H
Capasitor	680 μ F
Resistive Load	20 Ohm

In the simulation, the PI controls use K_p constant of 10 and T_i of 0.1. Simulation results under steady state are shown in Figure. 6. Simulation results using switching frequency of 20 kHz are shown in Figure. 6(a) and switching frequency of 100 kHz in Figure. 6(b). Figure. 6 shows that the boost converter output voltage is a sinusoidal with DC bias of 320 V that is greater than the input voltage. Figure. 6 also shows that the boost inverter control can achieve stable condition. Comparing Figures. 6(a) to 6(b), it can be seen that increasing the switching frequency can reduce the ripples in the inductor currents, boost converter output voltage, and boost inverter output voltage. When frequency of 20 kHz is used, the THD (Total Harmonic Distortion) of the boost inverter output voltage is 1.87%, while frequency of 100 kHz produces THD of 0.25%.

The simulation results during transient state are shown in Figure. 7. In Figure. 7, we can see two transient states, i.e. start-up from zero second and load step from 20 Ohm to 10 Ohm in 0.1 second. From Figure. 7, it is known that the steady state can be achieved in less than one fundamental period.

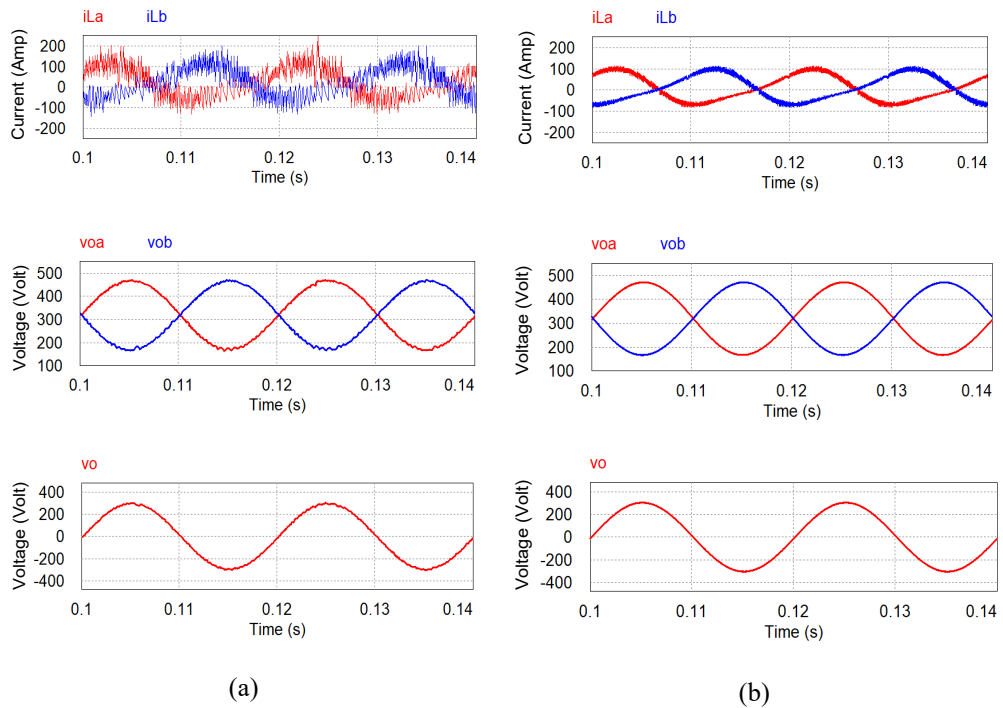


Figure 6. Simulation results during steady state (a) sampling frequency of 20 kHz (b) sampling frequency of 100 kHz

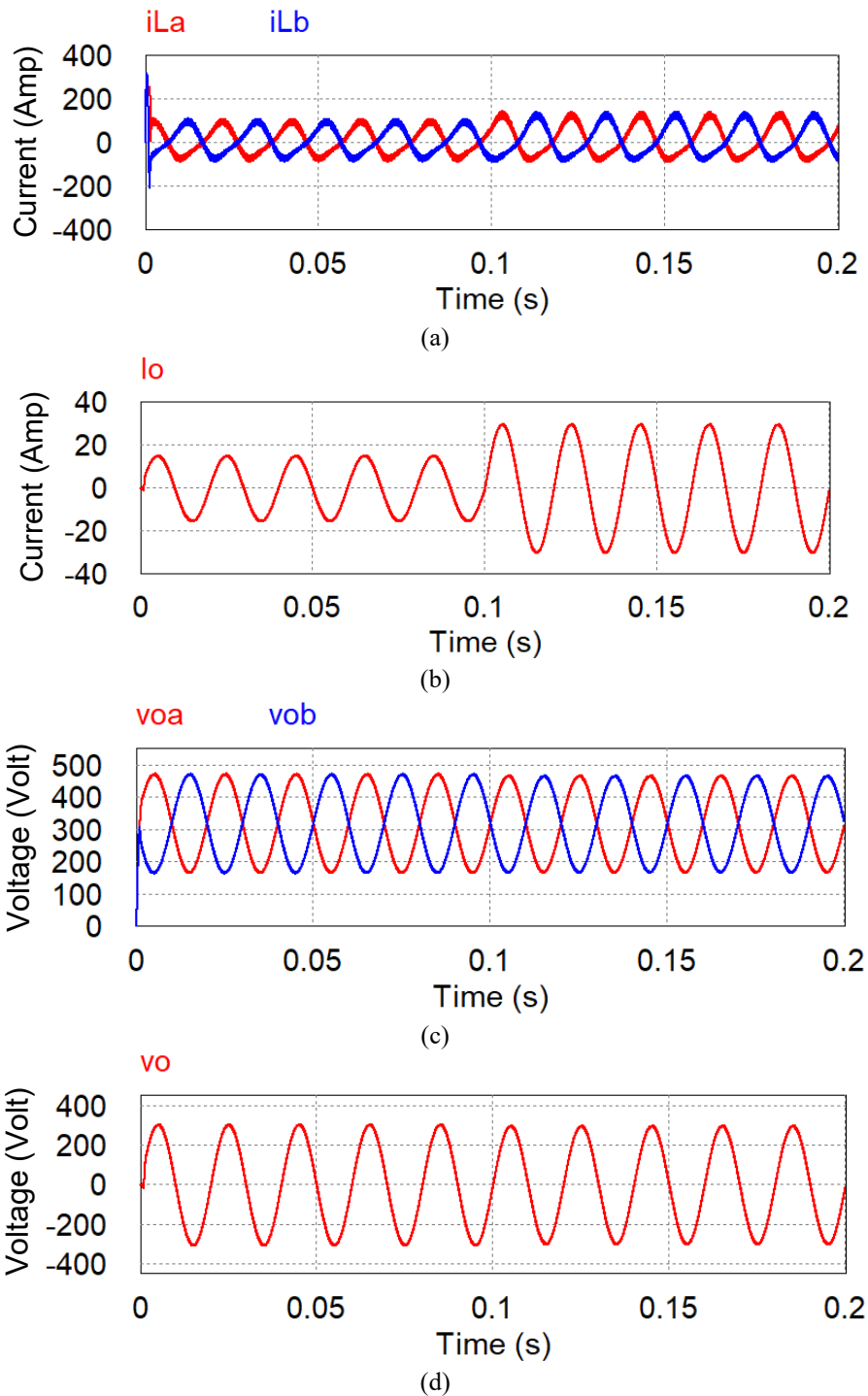


Figure 7. Simulation results with transient state (a) inductor current, (b) load current, (c) boost converter, and (d) boost inverter output voltage

5. Experimental Results

The parameters for the laboratory experiments are shown in Table 2. The PI control constants are $K_p = 0.15$ and $K_i = 0.05$. The overall control algorithm is implemented in a digital signal processor TMS28335 from Texas Instruments. The voltage references are

$$v_{oaref} = 10 + 3.5 \sin(100\pi t) \quad (24)$$

$$v_{obref} = 10 - 3.5 \sin(100\pi t) \quad (25)$$

Table 2. Experimental parameters

Parameters	Value
Input voltage	5 V
Output voltage amplitude	7 V
Output fundamental frequency	50 Hz
Inductor	150 μ H
Capasitor	680 μ F
Resistive Load	20 Ohm

The experimental results are shown in Figure. 8. Figure. 8(a) shows the inductor current of each converter dc-dc boost. As in the simulation, the inductor current ripple looks so obvious. The output voltage of each dc-dc boost converter is shown in Figure. 8(b). The waveform is not pure sinusoidal. It is similar to the results in [6]. The output voltage is shown in Figure. 8(c). The ripple that is visible on the output voltage is caused by low sampling frequency of 20 kHz. The FFT (Fast Fourier Transform) of the boost inverter output voltage is shown in Figure. 8(d). The output voltage amplitude at 50 Hz is 6.85 V and the THD is 5.33%. By using higher frequency, it is expected that the THD of boost inverter output voltage becomes lower, as shown by the simulation results.

The experiments were also conducted to verify the robustness of the hybrid control when the parameter values changed. The inductance variation experiments were conducted by varying the inductance values on the digital signal processor from 120 μ H to 180 μ H (in the range of 20% of the nominal value 150 μ H). On the other hand, the real inductors used in the boost inverter are remain unchanged, i.e. 150 μ H. The experimental results are shown in Table 3. It can be seen that the deviation value of inductance has no effect on the resulting control. The output voltage THD only change less than 1%, that is below the measurement precision.

The capacitance variation experiments were done by varying the capacitance values on digital signal processor from 544 μ F to 816 μ F (in the range of 20% of the nominal value of 680 μ F). The capacitances that were used in the boost inverter remain unchanged, i.e. 680 μ H. The experimental results are shown in Table 4. Similar to the inductance, the deviation value of the capacitance has no effect on the output voltage THD.

Table 3. Inductance variation effect

Inductance (μ H)	Magnitude at 50Hz (Volt)	THD (%)
120	6.69	5.79
135	6.72	4.97
150	6.67	6.04
165	6.59	5.49
180	6.65	5.47

Table 4. Capacitance variation effect

Capacitance (μF)	Magnitude at 50Hz (Volt)	THD (%)
544	6.67	5.95
612	6.66	6.06
680	6.71	4.64
748	6.61	6.41
816	6.67	6.66

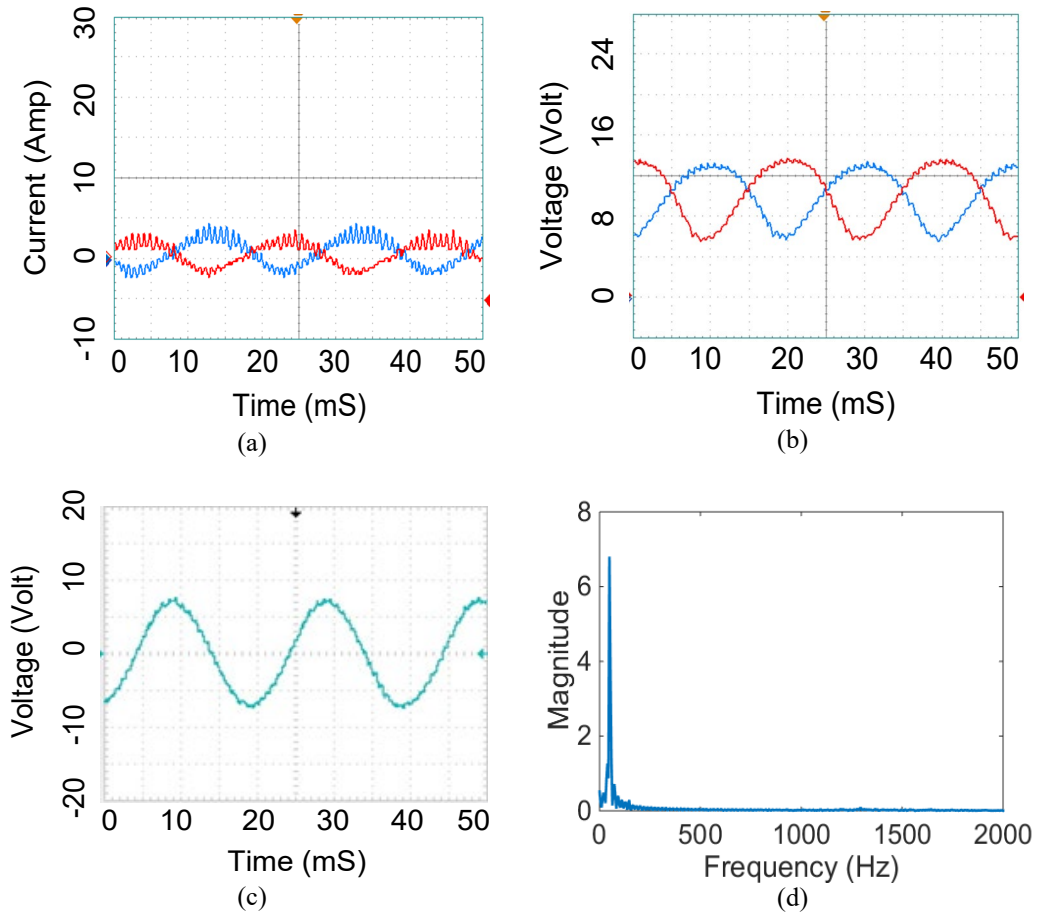


Figure 8. Boost inverter experimental results: (a) the inductor current (b) the output voltages of dc-dc boost converters, (c) the boost inverter output voltage, and (d) the spectrum of boost inverter output voltage

6. Conclusion

A hybrid control has been proposed to control a boost inverter. Simulations and experiments have been conducted to verify the performance of the boost inverter control. The simulation and experimental results show that the steady state can be achieved in less than one fundamental period. The proposed hybrid control is robust despite of load variation, inductance variation, and capacitance variation. The constraint of the hybrid control implementation is that it need long

time to calculate the overall algorithm. In order to get a good response, i.e. lower THD, faster digital processor is needed.

7. Acknowledgement

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