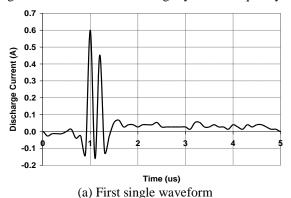
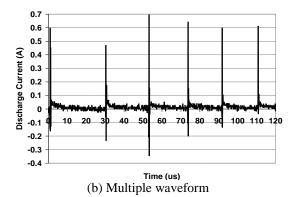
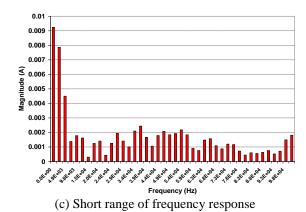
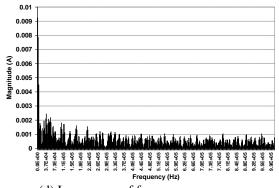
Figure 11 shows the second scenario of pulse burden circuits. In this circuit, it was pure resistive burden that parallel an 2.7 M Ω and 300 k Ω resistors, and in series with resistor of 2.7 M Ω .

Figure 12 shows the first sample current waveform of measurement result for the second scenario of pulse burden circuit. Figure 12(a) is the first single waveform of discharge pulse. In this waveform, the discharge pulse was occurred in time of around 1 μ s, and the specific discharge wave would cease in time of around 2 μ s. Figure 12(b) is the multiple waveform of discharge pulse. In this waveform, the discharge pulses were occurred in time of 30, 54, 74, 92 and 111 μ s, after the first pulse. The average different time among pulses was 20 μ s. Figure 12(c) is the short range of frequency response. The magnitude would increase locally in frequency of 26855, 36621 and 53711 Hz, and Figure 12(d) is the long range one. The magnitudes of discharge current would decrease slightly as the frequency increased.





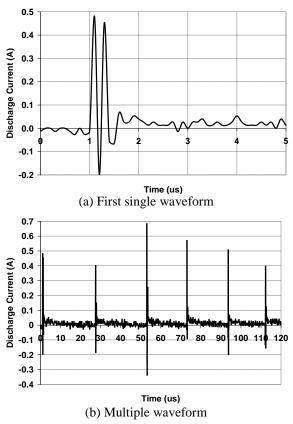


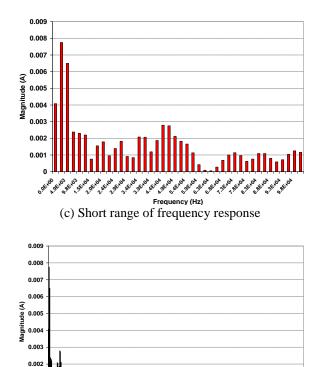


(d) Long range of frequency response

Figure 12. First sample current waveform result for the second scenario of pulse burden circuit

Figure 13 shows the second sample current waveform of measurement result for the second scenario of pulse burden circuit. Figure 13(a) is the first single waveform of discharge pulse. In this waveform, the discharge pulse was occurred in time of around 1 μ s, and the specific discharge wave would cease in time of around 2 μ s. Figure 13(b) is the multiple waveform of discharge pulse. In this waveform, the discharge pulses were occurred in time of 27.5, 53, 73, 94 and 112 μ s, after the first pulse. The average different time among pulses was 22.24 μ s. Figure 13(c) is the short range of frequency response. The magnitude would increase locally in frequency of 26855, 36621 and 46387 Hz, and Figure 13(d) is the long range one, where the magnitudes of discharge current would decrease slightly as the frequency increased.





(d) Long range of frequency response Figure 13. Second sample current waveform result for the second scenario of pulse burden

Figure 14 shows the third scenario of pulse burden circuits. In this circuit, it was pure 2.7 M Ω resistive burden that series with 300 k Ω resistor and series 15x10 μF in parallel. Those component were parallel connection with the resistor of 2.7 M Ω .

0.001

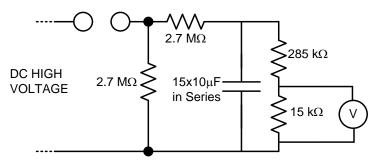
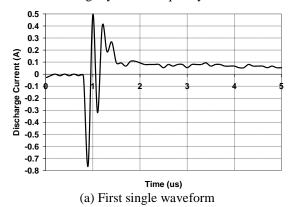
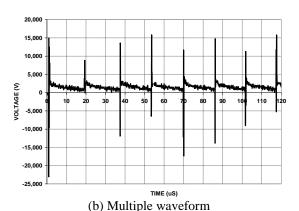


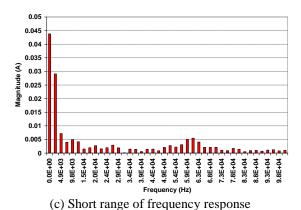
Figure 14. Third scenario of pulse burden as short capacitive-resistive circuit

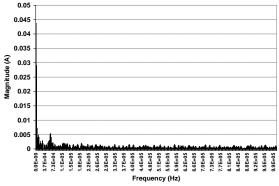
Figure 15 shows the first sample current waveform of measurement result for the third scenario of pulse burden circuit. Figure 15(a) is the first single waveform of discharge pulse. In this waveform, the discharge pulse was occurred in time of around 1 μ s, and the specific discharge wave would cease in time of around 2 μ s. Figure 15(b) is the multiple waveform of discharge pulse. In this waveform, the discharge pulses were occurred in time of 27.5, 53, 73,

94 and 112 μ s, after the first pulse. The average different time among pulses was 22.24 μ s. Figure 15(c) is the short range of frequency response. The magnitude would increase locally in frequency of 61035 Hz, and Figure 15(d) shows the long range one, where the magnitudes of discharge current would decrease slightly as the frequency increased.





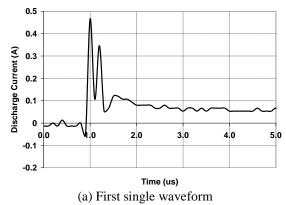


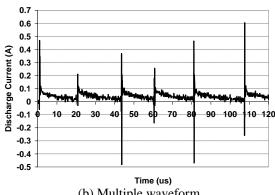


(d) Long range of frequency response

Figure 15. First sample current waveform result for the third scenario of pulse burden circuit

Figure 16 shows the second sample current waveform of measurement result for the third scenario of pulse burden circuit. Figure 16(a) is the first single waveform of discharge pulse. In this waveform, the discharge pulse was occurred in time of around 1 µs, and the specific discharge wave would cease in time of around 2 us. Figure 16(b) is the multiple waveform of discharge pulse. In this waveform, the discharge pulses were occurred in time of 21, 43.6, 61, 81 and 107.6 µs, after the first pulse. The average different time among pulses was 21.32 µs. Figure 16(c) is the short range of frequency response. The magnitude would increase locally in frequency of 36621 and 48828 Hz, and Figure 16(d) is the long range, that similar to the previous one.





(b) Multiple waveform

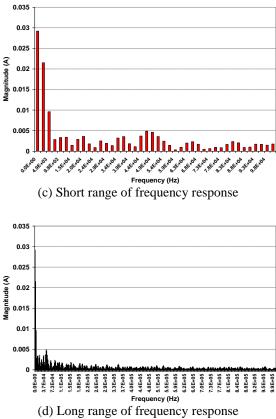


Figure 16. Second sample current waveform result for the third scenario of pulse burden circuit

Figure 17 shows the fourth scenario of pulse burden circuit. In this circuit, it was pure 2.7 M Ω resistive burden that series with 300 k Ω resistor that shunted by 15x10 μ F in parallel.

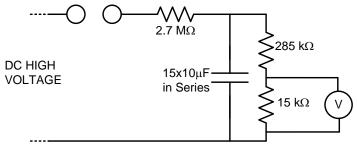
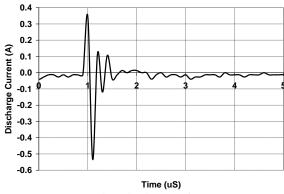


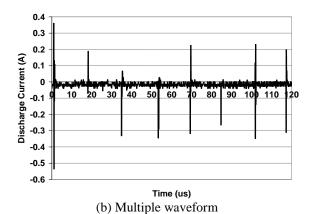
Figure 17. Fourth scenario of pulse burden as long capacitive-resistive circuit

Figure 18 shows the first sample current waveform of measurement result for the fourth scenario of pulse burden circuit. Figure 18(a) shows the first single waveform of discharge pulse. In this waveform, the discharge pulse was occurred in time of around 1 μ s, and the specific discharge wave would cease in time of around 2 μ s. Figure 18(b) shows the multiple waveform of discharge pulse. In this waveform, the discharge pulses were occurred in time of 0.9, 21.1, 39.3, 56.1, 72.3, 89.9, 104.3 and 118.3 μ s, after the first pulse. Nevertheless, almost pulses were negative values. The average different time among pulses was 16.8 μ s. Figure 18(c) is the short range of frequency response. The magnitude would increase locally in frequency of

26855 Hz, and Figure 18(d) is the long range one, where the current magnitudes remained relatively high.



(a) First single waveform



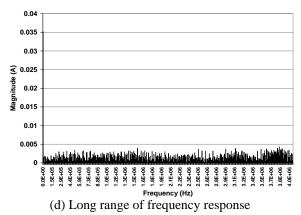
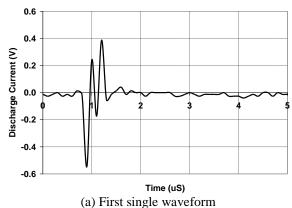
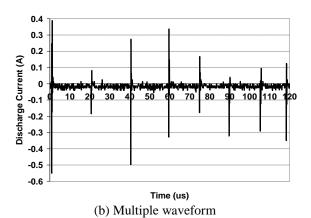
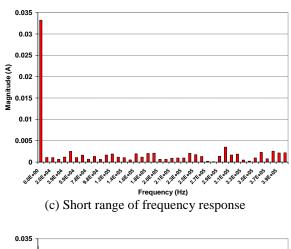


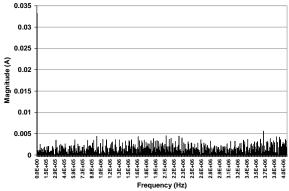
Figure 18. First sample current waveform result for the fourth scenario of pulse burden circuit

Figure 19 shows the second sample current waveform of measurement result for the fourth scenario of pulse burden circuit. Figure 19(a) is the first single waveform of discharge pulse. In this waveform, the discharge pulse was occurred in time of around 0.75 μ s, and the specific discharge wave would cease in time of around 1.5 μ s. Figure 19(b) is the multiple waveform of discharge pulse. In this waveform, the discharge pulses were occurred in time of 0.9, 20.7, 40.5, 59.5, 74.9, 89.7, 105.3 and 118.3 μ s, after the first pulse. Nevertheless, almost pulses were negative values. The average different time among pulses was 16.8 μ s. Figure 19(c) is the short range of frequency response. The magnitude would increase locally in frequency of 48828 Hz, and Figure 19(d) is the long range one, where the current magnitudes remained relatively high.









(d) Long range of frequency response Figure 19. Second sample current waveform result for the fourth scenario of pulse burden circuit

Table 1. Time of discharge and frequency response due to some circuit scenarios

		rable 1. Time of discharge and frequency response due to some circuit sectiatios					
Scenario	Impulse burdens	Times of discharge (μs)	Averages different time of discharge (μs)	Frequencies of increasing magnitude locally (Hz)			
1	Short pure resistive	31, 51, 67, 86, 101, 116	17	34180, 43945, 61035			
	circuit	26, 45, 66, 83, 98, 111	17	31738, 48828, 61035			
	Long pure	30, 54, 74, 92, 111	20	26855, 36621, 53711			
2 resistive circuit	27.5, 53, 73, 94, 112	22.24	26855, 36621, 46387				
3	Short resistive- capacitive	27.5, 53, 73, 94, 112	22.24	61035			
	circuit	21,43.6, 61, 81, 107.6	21.32	36621, 48828			
4	Short resistive-	0.9, 21.1, 39.3, 56.1, 72.3, 89.9, 104.3, 118.3	16.8	26855			
4 capacitive circuit	0.9, 20.7, 40.5, 59.5, 74.9, 89.7, 105.3, 118.3	16.8	48828				

Based on some discharge waveforms of testing results, it is observed that the waveforms with the pure resistive burdens would trend to be symmetrical to almost on positive parts. On

the other hand, the waveforms with the resistive and capacitive dominated burdens would be shorter, i.e. around 16.8 μ s, than those resistive dominated burdens, i.e. around 17 μ s – 22.24 μ s in average. The wave frequency response of discharge on the capacitive dominated burdens would be more declivous than those on the resistive dominated burdens. The latter characteristics are indicated by the specific capacitive dominated property. Table 1 lists the tabulation of the time of discharge and frequency response due to some circuit scenarios. Based on the table, the dominated capacitive existence made the repetitive discharge would be shorter than those the dominated resistive existence. The capacitive property that store charge was expected as cause the latter characteristics.

4. Conclusion

Almost all of the first discharge were occurred in around from 1 μs to 2 μs . The waveforms with the pure resistive burdens would trend to be symmetrical to almost on positive parts. On the other hand, the waveforms with the resistive and capacitive dominated burdens would be shorter than those resistive dominated burdens. The wave frequency response of discharge on the capacitive dominated burdens would more declivous than those on the resistive dominated burdens. The latter characteristics are indicated by the specific capacitive dominated property. The dominated capacitive existence made the repetitive discharge would be shorter than those the dominated resistive existence. The capacitive property that store charge was expected as cause the latter characteristics.

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6. References

- [1]. Wadhwa, C.L., "High Voltage Engineering (Second Edition)", *New Age International (P) Limited, Publishers*, 2007, ISBN: 978-81-224-2323-5, pp.81-104.
- [2]. Lucas, J.R., "High Voltage Engineering, Department of Electrical Engineering", *University of Moratuwa, Sri Lanka, Revised Edition*, 2001, pp.8-147.
- [3]. Kuffel, E., Zaengle, W.S., Kuffel, J., "High Voltage Engineering Fundamentals", *Newnes, Butterworth-Heinemann*, 2000, ISBN: 7506-3634-3, pp.48-75.
- [4]. Naidu, MS., Kamaraju, V., "High Voltage Engineering", Second Edition, *McGraw-Hill*, 1996, ISBN: 1996, ISBN: 0-07-462286-2, pp.129-150.
- [5]. Edirisinghe, M., "Nonlinear Load and RLC Pulse Shaping Surge Generator Models in Simulation Environment", *International Letters of Chemistry, Physics and Astronomy*, Vol. 36(2014), SciPress Ltd, Switzerland, pp.334-347.
- [6]. Schon, K., "High Impulse Voltage and Current Measurement Techniques", *Springer International Publishing, Switzerland* 2013, ISBN: 978-3-319-00377-1, pp.5-30.
- [7]. Holtzhausen JP., Vosloo, WL., "High Voltage Engineering", *Practice and Theory, Draft Version of Book*, ISBN: 978-0-620-3767-7, pp.86-91.
- [8]. Ramleth Sheeba, Madhavan Jayaraju, Thangal Kunju Nediyazhikam Shanavas, "Simulation of Impulse Voltage Generator and Impulse Testing of Insulator using MATLAB Simulink", ISSN 1 746-7233, World Journal of Modelling and Simulation, England, UK, Vol. 8 (2012) No. 4, pp. 302-309.
- [9]. Steven E. Meiners, J. R. Boston, H. K. Kim, R. G. Colclaser, "An Impulse Generator Simulation Circuit", *Thesis at Electrical Engineering*, University of Pittsburgh, November 25, 2002.
- [10]. Y Choyal, Lalit Gupta, Preeti Vyas, Prasad Deshpande, Anamika Chaturvedi, K C Mittal dan K P Maheshwari, "Development of a 300-kV Marx generator and its application to drive a relativistic electron beam", *Sadhana* Vol. 30, Part 6, December 2005, pp. 757–764, India.

- [11]. Muhammad Saufi Kamarudin, Erwan Sulaiman, Md Zarafi Ahmad, Shamsul Aizam Zulkifli and Ainul Faiza Othman, "Impulse Generator and Lightning Characteristics Simulation using Orcad PSpice Software", *Proceedings of EnCon2008*, 2nd Engineering Conference on Sustainable Engineering Infrastructures Development & Management, December 18-19, 2008, Kuching, Sarawak, Malaysia.
- [12]. Takayoshi Nakata, Yoshiyuki Ishihara dan Tadataka Moriyasu, "Calculation of Circuit Constants for Impulse Voltage Generator by Means of Computer", *Memoirs of The School of Engineering*, Okayama University, Vol. 1, No.1, March, 1966.
- [13]. Jari Hallstro, "A Calculable Impulse Voltage Calibrator, Acta Polytechnica Scandinavia", Electrical Engineering Series No. 109, Espoo 2002, 107, *Finnish Academies of Technology*, Finland, 2002, ISBN: 951-22-6174-X, pp.60-70.
- [14]. Yeong-Jer Chen, B.S.E.E., Andreas Neuber, John Mankowski, "Compact Repetitive Marx Generator and HPM Generation with the Vircator", *Electrical Engineering Graduate*, Faculty of Texas Tech University, December, 2005.
- [15]. A.A. Hueiit, E. Bautista, L.J. Villegast Anij M. Villagiian, "High voltage pulse generators and spark gap for gaseous discharge control", *Instrumentación Revista Mexicana de Física* 41, *No.* 3,1995, pp.08-.18.
- [16]. Ju-Hong Eom, Sung-Chul Cho† and Tae-Hyung Lee*, "Parameters Optimization of Impulse Generator Circuit for Generating First Short Stroke Lightning Current Waveform", *J Electr Eng Technol* Vol. 9, No. 1: 286-292, 2014.
- [17]. Zhu, Y., Zuegel, J.D., Marciante, J.R., Wu, H., "Distributed Waveform Generator: A New Circuit Technique for Ultra-Wideband Pulse Generation, Shaping and Modulation", *IEEE Journal of Solid-State Circuits*, Vol. 44, No. 3, March 2009, pp.808-823.
- [18]. Waluyo, Syahrial, Sigit Nugraha, Yudhi Permana JR, "Miniature Prototype Design and Implementation of Modified Multiplier Circuit DC High Voltage Generator", International Journal of Electrical Engineering & Technology (IJEET), International Association for Engineering and Management Education (IAEME), Volume 6, Issue 1, January (2015), pp. 01-12.



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