

His research interest includes Design of high frequency power conversion circuits and Applications of Soft Computing Techniques, improvement in Multilevel Converter Topology, Power Factor Improvement, Power quality Improvement in power system and Electric drives.



Nishant Patnaik received the B.Tech in Electrical Engineering from BIT Durg, India and M.Tech in Power Electronics and Drives from Kalinga Institute of Industrial Technology, Bhubaneswar, India in 2006 and 2009 respectively. He completed his Ph.D from National Institute of Technology Rourkela, India in 2017. He has published several articles in reputed journals and conferences. His current research includes power quality improvement and application of power electronics with non-conventional energy sources.